REPLACEMENT SHEET

1 / 10

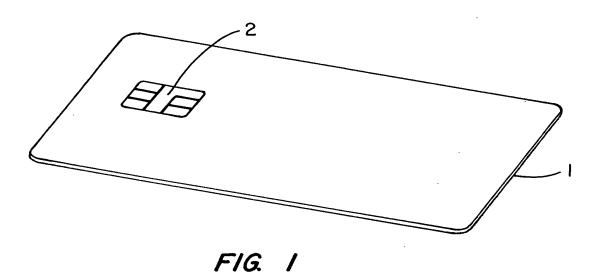
Finis CONNER, et al.

"Secure Transaction Card with a Large Storage Volume"

Attorney Docket No. 021206-000710US

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1/10



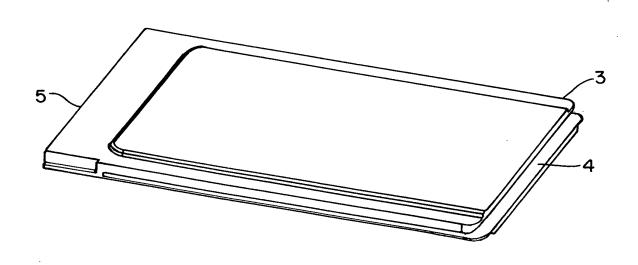


FIG. 2

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Finis CONNER, et al. Application No. 10/716,267
"Secure Transaction Card with a Large Storage Volume"

Attorney Docket No. 021206-000710US

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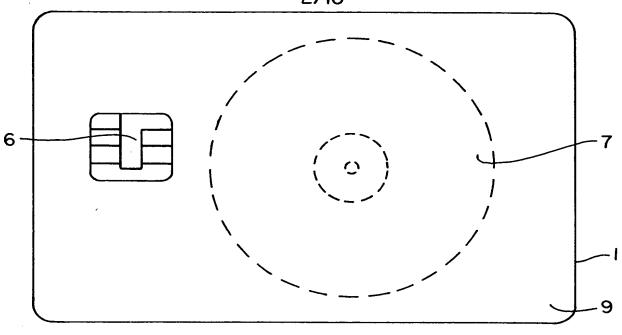
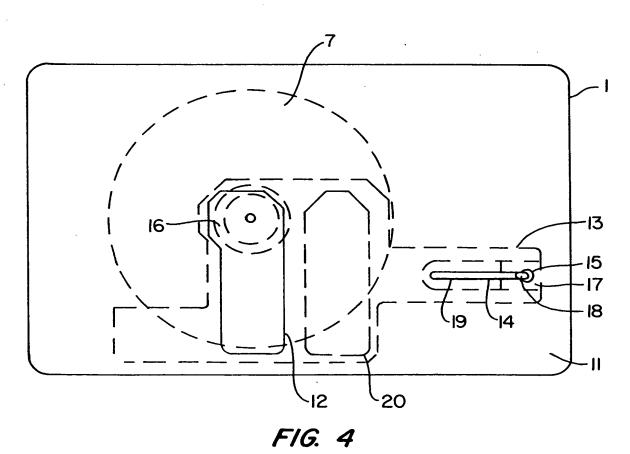


FIG. 3

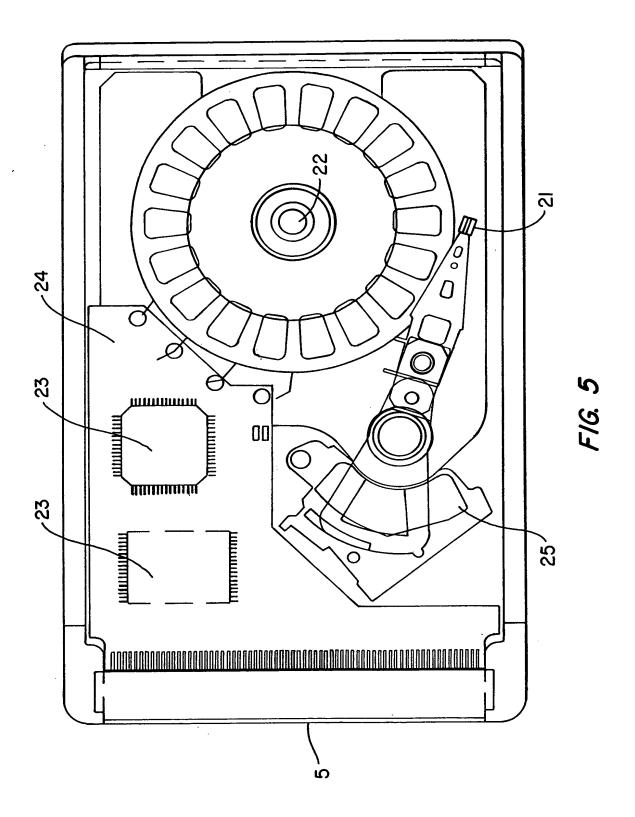


Finis CONNER, et al.

"Secure Transaction Card with a Large Storage Volume"

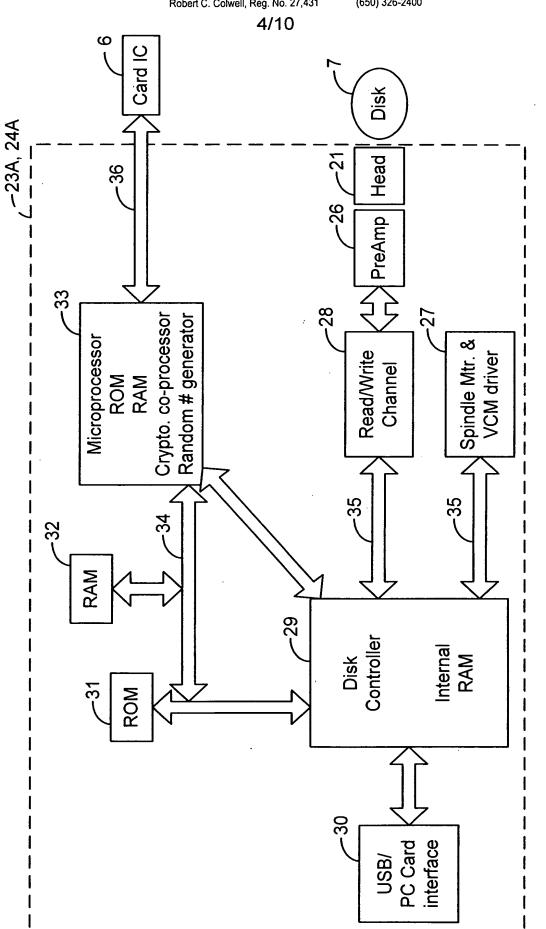
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"Secure Transaction Card with a Large Storage Volume"

Attorney Docket No. 021206-000710US Robert C. Colwell, Reg. No. 27,431 (650) 326-2400



F/G. 6

Finis CONNER, et al.

"Secure Transaction Card with a Large Storage Volume"

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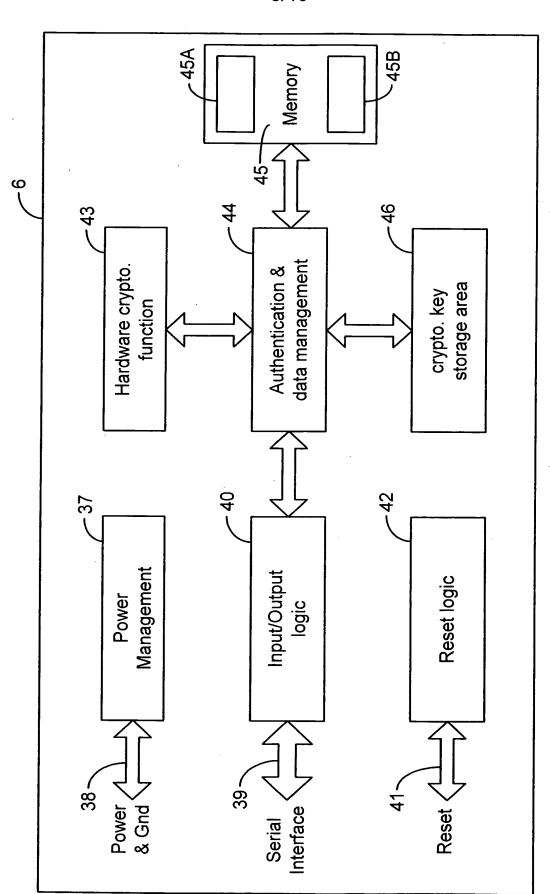


FIG. 7

Finis CONNER, et al. Application No. 10/716,267 "Secure Transaction Card with a Large Storage Volume" Attorney Docket No. 021206-000710US Robert C. Colwell, Reg. No. 27,431 (650) (650) 326-2400 6/10 -47 -33 **ROM** 48 Microprocessor 50 54 **RAM External RAM** -51 55 High speed Crypto. ISO7816 interface Co-processor or logic 52 56-Interrupt controller On chip security -53 57-Random # Generator **Timers** - 49

FIG. 8

Power Management

REPLACEMENT SHEET

7 / 10

Finis CONNER, et al.

Application No. 10/716,267

"Secure Transaction Card with a Large Storage Volume"

Attorney Docket No. 021206-000710US Robert C. Colwell, Reg. No. 27,431 (650) 326-2400

7/10

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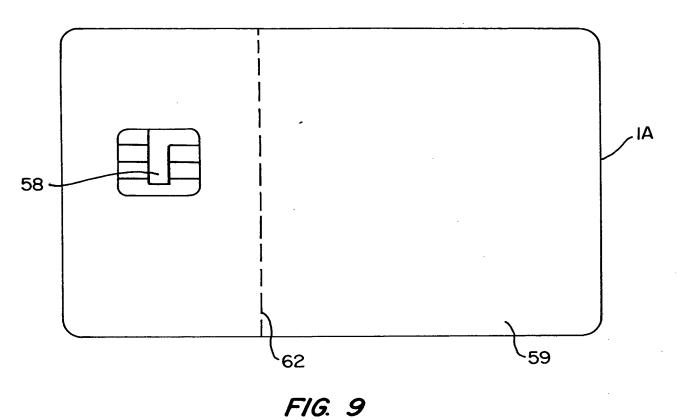






FIG. 10

Finis CONNER, et al.

CONNER, et al. Application No. 10/716,267
"Secure Transaction Card with a Large Storage Volume"

Attorney Docket No. 021206-000710US
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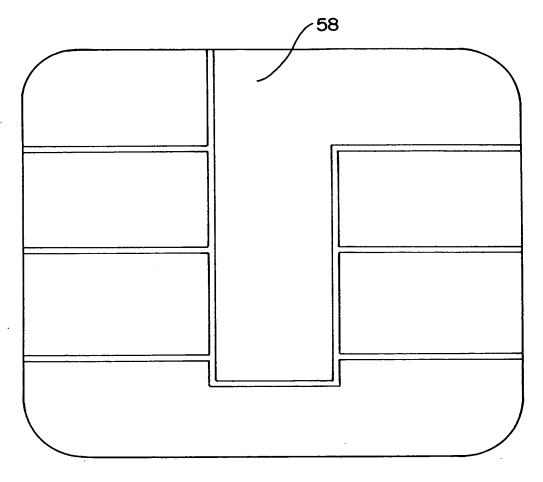
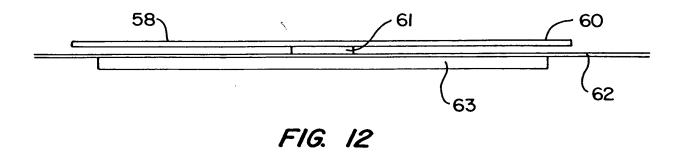


FIG. II



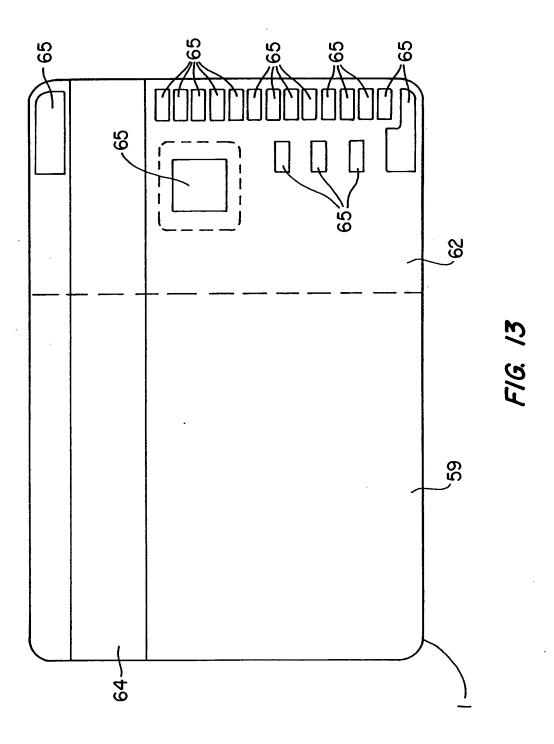
9 / 10

Finis CONNER, et al.

CONNER, et al. Application No. 10/716,267
"Secure Transaction Card with a Large Storage Volume"

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Application No. 10/716,267

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